



Attorney's Docket No.: 10417-120001 / F51-143214M/SW

*IFLW AF*  
*2825*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al.

Art Unit : 2825

Serial No. : 10/080,823

Examiner : Phallaka Kik

Filed : February 21, 2002

Title : PATTERN LAYOUT METHOD OF SEMICONDUCTOR DEVICE

**MAIL STOP AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MARCH 15, 2004

Please amend the above-identified application as follows:

*Please Enter this  
amendment after final  
6/17/04*  
*(P)*

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

June 4, 2004

Date of Deposit

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